

## Application Data Sheet

### **Application Information**

Application Type:: Regular  
Subject Matter:: Utility  
Title:: SEMICONDUCTOR INTEGRATED CIRCUIT  
HAVING VOLTAGE-DOWN CIRCUIT REGULATOR  
AND CHARGE SHARING  
Attorney Docket Number:: 03-0833/L13.12-0243  
Request for Non-Publication?:: No  
Suggested Drawing Figure:: FIG. 2  
Total Drawing Sheets:: 4  
Small Entity?:: No  
Petition included?:: No  
Petition Type::

### **Applicant Information**

Applicant Authority Type:: Inventor  
Primary Citizenship Country:: US  
Given Name:: Michael N.  
Family Name:: Dillon  
Name Suffix::  
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State or Province of Residence:: MN  
Country of Residence:: US  
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City of Mailing address:: Richfield  
State of Province of mailing address:: MN  
Country of mailing address::  
Postal or Zip Code:: 55423

Applicant Authority Type:: Inventor  
Primary Citizenship Country:: US  
Given Name:: Bret A.  
Family Name:: Oeltjen  
Name Suffix::  
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Street of Mailing address:: 6165 East 250<sup>th</sup> Street  
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#### **Correspondence Information**

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LSI Logic Corporation  
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State or Province of mailing address:: CA  
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#### **Representative Information**

Representative Customer Number::	000024319	
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#### **Assignee Information**

Assignee name:: LSI Logic Corporation  
Street of mailing address:: 1621 Barber Lane  
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City of mailing address:: Milpitas  
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